

FOR IMMEDIATE RELEASE:

Embedded Systems Conference (ESC) Moscone Center, Booth 741 March 7-10, 2005 **Media Contact:**

Barbara Stewart Patterson & Associates Tel: 480-488-6909

Email: barbara@patterson.com

Macraigor Systems Removes Price Barriers to Boundary-Scan Testing with New J-Scan Debugging and Programming Tool

Accelerates Development Cycle and Prototyping Process
Instantly Programs IC Devices, FPGAs and CPLDs

SAN FRANCISCO, CA–March 7, 2005–Macraigor Systems LLC, a leading supplier of Joint Test Action Group (JTAG) and background debug mode (BDM) connection solutions for on-chip debugging, today announced the availability of J-Scan Version 1.0, a new high-speed, low-cost boundary-scan debug and programming tool. This new technology allows circuit designers to facilitate early test development, thereby shortening the development cycle and prototyping process.

Unlike logic analyzers and oscilloscope probes, J-Scan permits designers to see what all the pins under a ball grid array (BGA) device are doing in real time on their PCs or laptops. The J-Scan debug and programming tool also allows designers to manually force the pins to any logic state with a simple point and click of the mouse. For the first time, users can finally see what every pin under a BGA is doing. J-Scan instantly answers questions such as: Is the oscillator connected to pin H17 under the BGA? Is the address bus active? Is the data bus being driven? Are any lines shorted?

With J-Scan Version 1.0, the IC designer now has control of every pin. If the CPU is not yet available and the designer needs to program flash memory, programming is as easy as setting up the signals (address, data, enables), selecting a data file and pressing the PROGRAM button. The provided USB 2.0 download cable permits programming times of minutes, not hours. Utilities to program field-programmable gate arrays (FPGAs) and complex programmable logic devices (CPLDs) are also available.

-more-

"J-Scan provides a quick, simple and effective way to help designers debug their circuits, perform non-destructive continuity testing and validate boundary-scan chain integrity independent of the their present design tools. This is a natural extension of the existing Macraigor's debug technology," said Craig Haller, chief engineer at Macraigor Systems LLC. "The best news is we have intentionally priced J-Scan at one-fifth of the competitive solution with all the same features so that every designer and technician who debugs circuit boards can have access to this powerful technology."

The J-Scan debug and programming tool is extremely easy to use, allowing designers to be up and running in minutes. The designer simply plugs in the Macraigor usb2Demon interface, drops any type of integrated circuit (IC) device on the screen and presses the scan button. Instantly, all activity on every boundary-scan enabled pin on any device or chain of devices is visible on the PC or laptop.

The J-Scan manual is written in a tutorial style that teaches users not only the fundamentals of J-Scan and how to simulate faults, but also the fundamentals of boundary scan. No prior boundary-scan experience is required. Multiple J-Scan video tutorials featuring a fully populated demonstration board are available online as well.

Pricing & Availability

Available for order now, J-Scan is priced at \$1,895 USD. The kit includes the J-Scan debug and programming software, USB 2.0 interface cable, demo board, power supply and Getting Started tutorial manual. For more information on this and other debug tools available from Macraigor Systems, visit www.Jscan.com.

About Macraigor Systems

Macraigor Systems is a leading supplier of JTAG/BDM connection solutions for on-chip debugging of 32 and 64-bit embedded microprocessors. Macraigor Systems' solutions are designed for price-sensitive customers. These solutions include a suite of software tools that supports Windows 9x, NT, ME, XP, 2000, Linux and Solaris host systems. Macraigor Systems supports all major embedded microprocessor architectures, including AMD, ARM, CPU32 Series, PowerPC, MIPS and the XScale microarchitecture. For more information about Macraigor products, please visit www.macraigor.com.

###

Macraigor Systems LLC, OCDemon and J-Scan are trademarks or registered trademarks of Macraigor Systems LLC in the U.S. and/or internationally. All other trademarks and products are the property of their respective owners.

North American Sales Contact: Macraigor Systems LLC, PO Box 471008, Brookline Village, MA 02447-1008, Tel: 617-739-8693; Fax: 617-739-8694, Email: http://www.macraigor.com/contact_sales.htm; Website: http://www.macraigor.com.