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Macraigor Systems Announces New JTAG and Flash Support for Freescale and MIPS Processors

BROOKLINE, MA–December 5, 2005–Macraigor Systems LLC today announced JTAG debug and Flash programming support for several new Freescale PowerPC and MIPS Technologies 64-bit processors. Full JTAG debug and Flash programming support has been added for the Freescale MPC5200 rev. B processor and the Freescale 83xx PowerQUICC II Pro family of processors.

Full JTAG Debugging and control of these processors will be supported by all Macraigor debug solutions when used with the OCDemon family of products. In addition, Macraigor's Flash Programming application has been updated to support the following:

- Freescale MPC5500 family of processors
- Freescale MAC71xx family of processors
- MIPS 5kc-based 64-bit processors, including:
 - Phillips PR1900
 - Toshiba TX49 family
 - Broadcom BCM1250 and BCM1125

Macraigor has also added support for these processors to the free, pre-built GNU tools suite to include sample configurations for standard evaluation boards. These examples contain source, gdbinit and make files for each board. The included demo program allows developers to easily build, download and debug a small application via gdb.

"The Macraigor debug tools provide developers with solutions that leverage the ever increasing processing power and bandwidth of the Freescale and MIPS processors," explained Craig Haller, chief engineer of Macraigor Systems LLC." At the same

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time, these tools provide the cost efficiencies that are essential to next-generation automotive control, consumer and telecommunications equipment manufacturers in this difficult economic environment."

Macraigor's OCDemon family of products offers the industry's most advanced, yet pricesensitive solution for designing, debugging and programming embedded hardware and application software.

Host Interface Flexibility & Price-Performance Ratio

Macraigor Systems' debugging solutions include a broad range of hardware interface devices and application programs. These provide embedded systems engineers with a high degree of flexibility in selecting a host interface and an appropriate price/performance ratio for their embedded projects. Hardware debug devices are available with parallel, serial, USB or Ethernet connectivity to both Windows and Linux host machines.

Debug application programs are available to support an embedded project through the entire development cycle, from troubleshooting a prototype hardware design to writing and debugging boot code, programming in-circuit Flash devices, developing application-level software and supporting manufacturing burn-in and testing.

Pricing & Availability

The OCDemon family of debug tools is immediately available for the ARM, MIPS and PowerPC processors. OCDemon Flash Programmer, Flash Access and Target Access are each \$500 for the first license, and incremental licenses are \$100 USD each. OCD Commander, an assembly-level software debugger, and OCD Remote, the GNU tools suite, are available at no charge from the Macraigor website.

About Macraigor Systems

Macraigor Systems LLC is a leading supplier of boundary scan-based hardware debug and BDM/JTAG connection solutions for on-chip debugging of 32- and 64-bit embedded microprocessors. Macraigor Systems' solutions are designed for price-sensitive customers. These debug solutions include a suite of software tools that support Windows 9x, NT, ME, XP, 2000, Linux and Solaris host systems. Macraigor Systems supports all major embedded microprocessor architectures, including AMD, ARM, CPU32 Series, PowerPC, MIPS and the XScale microarchitecture. For more information about Macraigor products, please visit www.macraigor.com.

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